

31011 U.S. PTO
10/050347

01/15/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10050347	01/15/2002	438	28.7	2813	SCHILLINGER

**APPLICANTS: Sandhu Gurtej; Moore John; Ruager Neal;

Rs

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/633,556 08/07/2000

Rs

** FOREIGN APPLICATIONS VERIFIED:

Rs

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no

ATTORNEY DOCKET NO

Verified and Acknowledged Examiners's initials

MI22-1897

TITLE : Method of forming transistors

U.S. DEPT. OF COMM/PAT. & TM-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
7/2/02		Total Claims 12 Print Claim for O.G. 1	
ISSUE FEE		DRAWING	
Amount Due 1580	Date Paid	Sheets Drwg. 3	Figs. Drwg. 16
		Print Fig. 2	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

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103 (encl)

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